



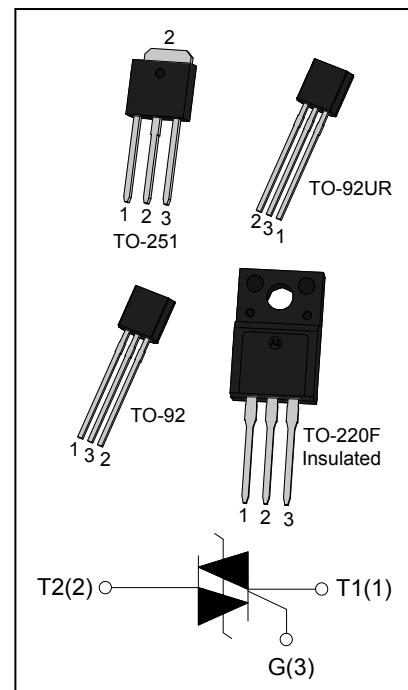
## ACJT2 Series 2A TRIACs

Rev.10.0

## DESCRIPTION:

With high ability to withstand the shock loading of large current, ACJT2 series triacs provide high dv/dt rate with strong resistance to electromagnetic interference. They are especially recommended for use on inductive load and serious electromagnetic interference place.

From all three terminals to external heatsink, ACJT2xx-xxF provides a rated insulation voltage of 2000 V<sub>RMS</sub>. All the packages listed above are RoHS compliant. (2011/65/EU)



## MAIN FEATURES

Symbol	Value	Unit
I <sub>T(RMS)</sub>	2	A
V <sub>DRM</sub> /V <sub>RRM</sub>	1000	V

## ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T <sub>stg</sub>	-40-150	°C
Operating junction temperature range	T <sub>j</sub>	-40-125	°C
Repetitive peak off-state voltage( T <sub>j</sub> =25°C )	V <sub>DRM</sub>	1000	V
Repetitive peak reverse voltage( T <sub>j</sub> =25°C )	V <sub>RRM</sub>	1000	V
RMS on-state current TO-251 (T <sub>c</sub> =105°C) TO-92/ TO-92UR (T <sub>c</sub> =90°C) TO-220F(Ins) (T <sub>c</sub> =100°C)	I <sub>T(RMS)</sub>	2	A
Non repetitive surge peak on-state current ( full cycle, F=50Hz )	I <sub>TSM</sub>	20	A
I <sup>2</sup> t value for fusing ( tp=10ms )	I <sup>2</sup> t	2	A <sup>2</sup> s
Rate of rise of on-state current (I <sub>G</sub> =2×I <sub>GT</sub> )	dI/dt	50	A/μs

Peak gate current	I <sub>GM</sub>	1	A
Average gate power dissipation	P <sub>G(AV)</sub>	0.1	W
Peak gate power	P <sub>GM</sub>	1	W

**ELECTRICAL CHARACTERISTICS (T<sub>j</sub>=25°C unless otherwise specified)**

Symbol	Test Condition	Quadrant	Value		Unit
			ACJT210	ACJT225	
I <sub>GT</sub>	V <sub>D</sub> =12V R <sub>L</sub> =33Ω	I - II -III	MAX	10	25 mA
V <sub>GT</sub>		I - II -III	MAX	1.3	V
V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> T <sub>j</sub> =125°C R <sub>L</sub> =3.3KΩ	I - II -III	MIN	0.2	V
I <sub>L</sub>	I <sub>G</sub> =1.2I <sub>GT</sub>	I -III	MAX	25	mA
		II		35	
I <sub>H</sub>	I <sub>T</sub> =100mA		MAX	10	40 mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125°C		MIN	600	1000 V/μs

**STATIC CHARACTERISTICS**

Symbol	Parameter		Value(MAX)	Unit
V <sub>TM</sub>	I <sub>TM</sub> =2.8A tp=380μs	T <sub>j</sub> =25°C	1.55	V
I <sub>DRM</sub>	V <sub>D</sub> =V <sub>DRM</sub> V <sub>R</sub> =V <sub>RRM</sub>	T <sub>j</sub> =25°C	10	μA
I <sub>RRM</sub>		T <sub>j</sub> =125°C	1	mA

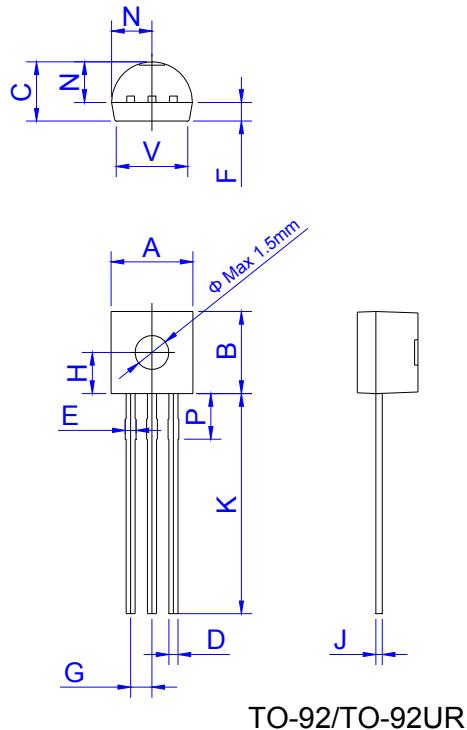
**THERMAL RESISTANCES**

Symbol	Parameter		Value	Unit
R <sub>th(j-c)</sub>	junction to case(AC)	TO-251	4.5	°C/W
		TO-92/TO-92UR	11.2	
		TO-220F(Ins)	7.5	

## ORDERING INFORMATION

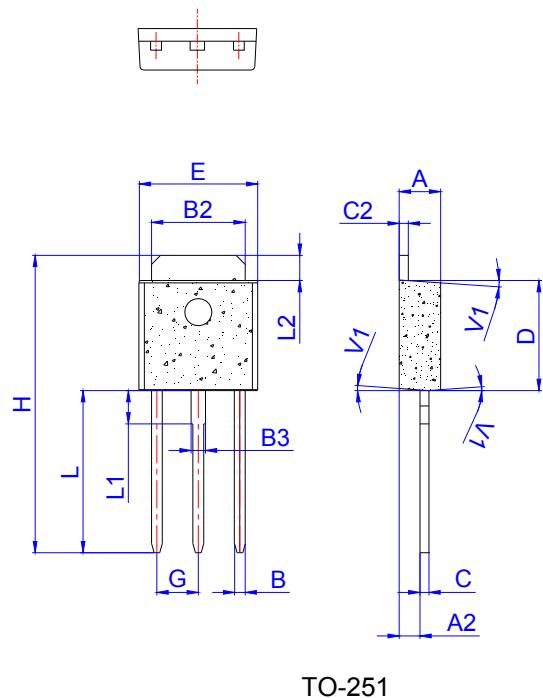
<b>AC</b>	<b>J</b>	<b>T</b>	<b>2</b>	<b>10</b>	<b>-10</b>	<b>U</b>
AC switch						U:TO-92 H:TO-251 UR:TO-92UR F:TO-220F(Ins)
JieJie Microelectronics Co.,Ltd		Triacs				
						10: $V_{DRM} \setminus V_{RRM} \geq 1000V$
						10: $I_{GT1-3} \leq 10mA$ 25: $I_{GT1-3} \leq 25mA$

## PACKAGE MECHANICAL DATA

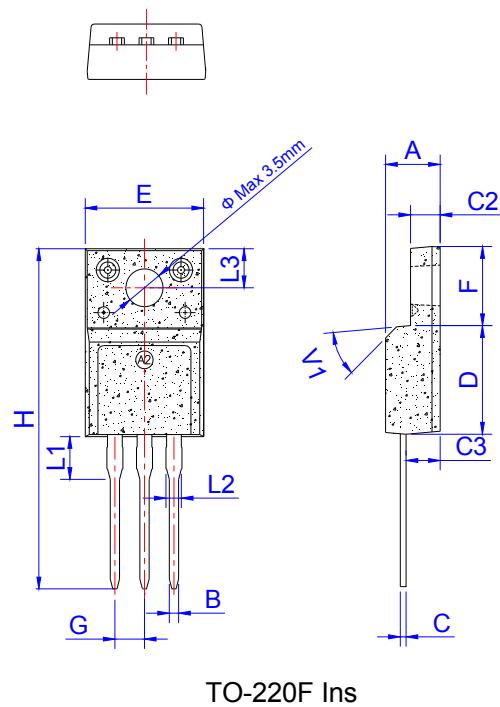


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.45		5.20	0.175		0.205
B	4.32		5.33	0.170		0.210
C	3.18		4.19	0.125		0.165
D	0.407		0.533	0.016		0.021
E	0.60		0.80	0.024		0.031
F	-	1.1	-	-	0.043	-
G	-	1.27	-	-	0.050	-
H	-	2.30	-	-	0.091	-
J	0.36		0.50	0.014		0.020
K	12.70		15.0	0.500		0.591
N	2.04		2.66	0.080		0.105
P	1.86		2.06	0.073		0.081
V	-		4.3	-		0.169

## PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.20		2.40	0.086		0.095
A2	0.90		1.20	0.035		0.047
B	0.55		0.65	0.022		0.026
B2	5.10		5.40	0.200		0.213
B3	0.76		0.85	0.030		0.033
C	0.45		0.62	0.018		0.024
C2	0.48		0.62	0.019		0.024
D	6.00		6.20	0.236		0.244
E	6.40		6.70	0.252		0.264
G		2.30				0.091
H	16.0		17.0	0.630		0.669
L	8.90		9.40	0.350		0.370
L1	1.80		1.90	0.071		0.075
L2	1.37		1.50	0.054		0.059
V1		4°				4°

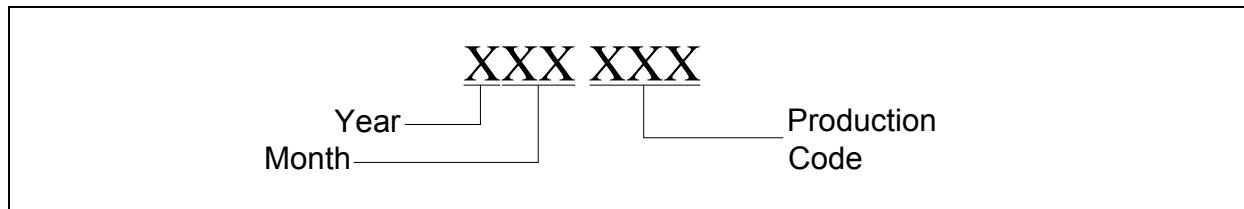
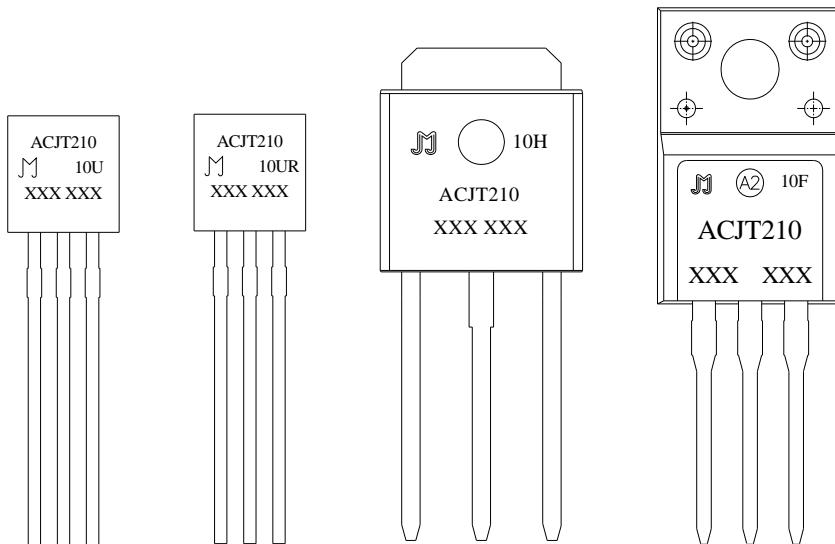


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.50		4.90	0.177		0.193
B	0.74	0.80	0.83	0.029	0.031	0.033
C	0.47		0.65	0.019		0.026
C2	2.45		2.75	0.096		0.108
C3	2.60		3.00	0.102		0.118
D	8.80		9.30	0.346		0.366
E	9.80		10.4	0.386		0.410
F	6.40		6.80	0.252		0.268
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.63			0.143	
L2	1.14		1.70	0.045		0.067
L3		3.30			0.130	
V1		45°			45°	

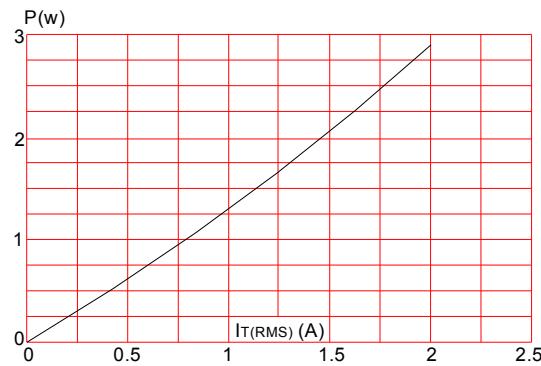
## PACKAGE INFORMATION

PACKAGE	OUTLINE	TUBE (PCS)	INNER BOX (PCS)	PER CARTON
TO-220F	TUBE	50	1,000	8,000
TO-251	TUBE	80	4,000	32,000
PACKAGE	OUTLINE	BAG (PCS)	INNER BOX (PCS)	PER CARTON
TO-92/ TO-92UR	Shielding Bag	1,000	10,000	30,000

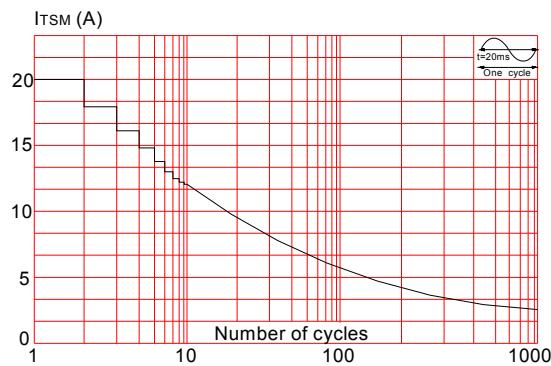
## MARKING



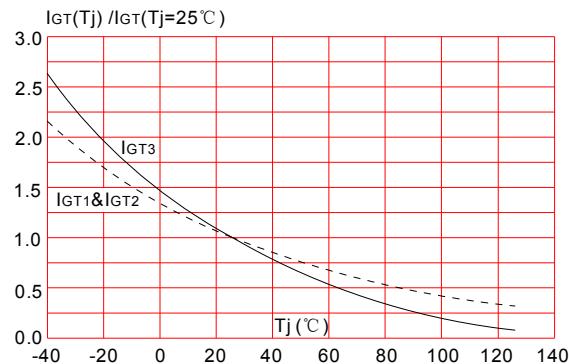
**FIG.1:** Maximum power dissipation versus RMS on-state current



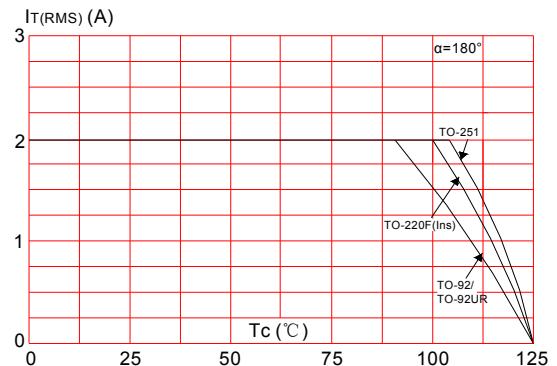
**FIG.3:** Surge peak on-state current versus number of cycles



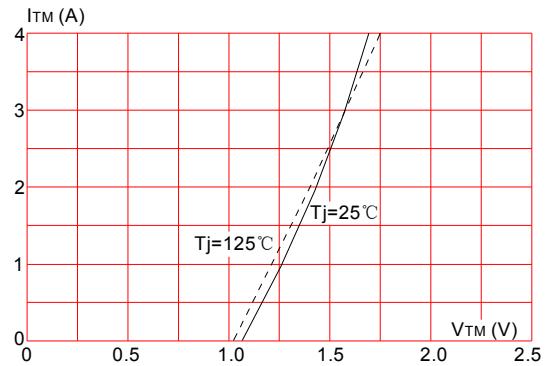
**FIG.5:** Relative variations of gate trigger current versus junction temperature



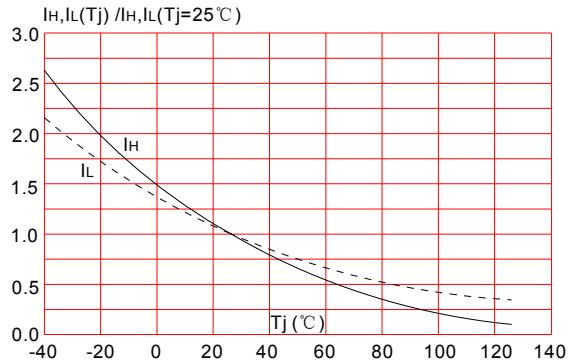
**FIG.2:** RMS on-state current versus case temperature



**FIG.4:** On-state characteristics (maximum values)



**FIG.6:** Relative variations of holding current, latching current versus junction temperature



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co.,Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.

Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information.

This document is the tenth version which is made in 18-Apr.-2019. This document supersedes and replaces all information previously supplied.



is a registered trademark of Jiangsu JieJie Microelectronics Co.,Ltd.

Copyright ©2019 Jiangsu JieJie Microelectronics Co.,Ltd. Printed All rights reserved.